

Velammal College of Engineering and Technology, Madurai – 625 009

(Autonomous)

MoU Details

Department of Electronics Engineering (VLSI Design and Technology)

Academic Year 2024-2025

S. No	Name of the Industry with Address	MoU Signed Date/Duration	Scope of MoU	Outcomes of MoU
1	Gnanodaya VLSI, No. 85, 12, 4th floor, Sri Sheshadri Complex, Dental College road, Manjunath Layout, Munnekollala, Marathahalli, Bangalore, Karnataka – 560037 Mobile: 9912340229	15.12.24 / Five Years	Training and Placement	Enhanced VLSI skills through specialized training, research support, and improved career opportunities via industry-linked placements and internships.