

**Velammal College of Engineering and Technology, Madurai – 625 009**  
**(Autonomous)**

**Patents Details**

**Department of Electronics Engineering (VLSI Design and Technology)**

**Academic Year 2024-2025**

<b>S. No</b>	<b>Categories (Design/Utility)</b>	<b>Title of the Invention</b>	<b>Application Number</b>	<b>Filing Date</b>	<b>Date of Publication</b>	<b>Status</b>
1	Utility	Indigenous Multi-Analyte Device for Detecting Adulteration in Liquid Edible Products	202441096040	05/12/2024	13/12/2024	Published
2	Utility	A Novel Drain Covered Shaped Gate with High-k TiO <sub>2</sub> of Nanosheet FETS	202441096071	05/12/2024	13/12/2024	Published
3	Utility	A Gate Terminal with a Dual Contacts for a HEMT and Manufacturing Method Thereof	202441032080	23/04/2024	10/07/2025	Granted
4	Utility	Exploring Machine Learning Models in Mobile-Integrated Vocabulary Learning for EFL Students in Higher Education	202441064134	26/08/2024	30/08/2024	Published